

REMARKS/ARGUMENTS

Reconsideration of the application in view of the above amendments and the following remarks is respectfully requested.

Paragraph [0018] of the specification has been amended to correct an obvious typographical error. No new matter has been added.

Claims 1-26 remain in this application. Claims 1 and 6 are amended.

Claims 1-11 and 24-26 were rejected under 35 U.S.C. § 112, second paragraph, as being indefinite. With respect to claim 1, the Examiner stated: "...it is not clear to what is meant by the added limitation 'selectively storing the ECC in a plurality of registers using a switching mechanism, the storing while transferring the data block.'" With respect to claim 6, the Examiner stated: "...it is not clear to what is meant by the limitations 'said controller is configured to shift a data block between the flash memory and the controller while computing an ECC for said data block.'"

Claims 1 and 6 have been amended to more clearly define the invention and, as amended, comply with the requirements of 35 U.S.C. § 112, second paragraph.

Accordingly, Applicants believe that the application, as amended is in condition for allowance, and such action is respectfully requested.

The Commissioner is hereby authorized to charge any required fees due (other than issue fees), and to credit any overpayment made, in connection with the filing of this paper to Deposit Account 20-0668 of Texas Instruments Incorporated.

Respectfully submitted,

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